

Figure 1

FIG. 2

The diagram illustrates the timing structure of a DVB-T2 frame. It consists of the following segments in microseconds (μs):

- Ramp-Up:** 10 μs
- FREQUENCY CORRECTION TRAINING:** 272 μs
- TIMING CORRECTION TRAINING:** 256 μs
- BCH PREAMBLE:** 16 μs
- BROADCAST (BCH) INFORMATION:** 512 μs
- Ramp-Down:** 10 μs
- Interburst Guard:** 14 μs

The total duration of the frame is 1090 μs.

Fig. 3

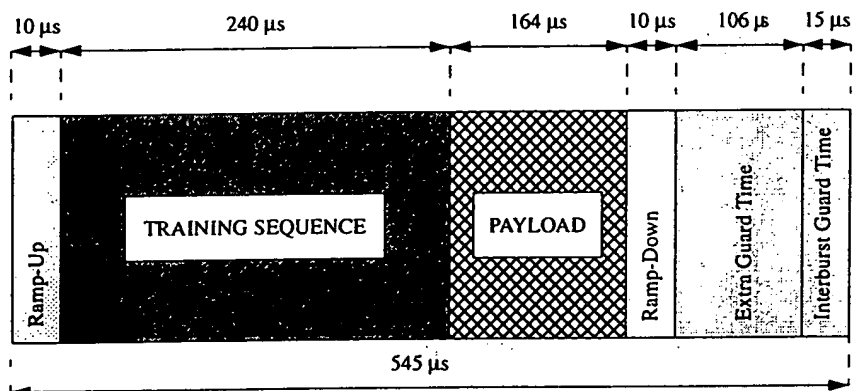


Fig. 4

Diagram illustrating a frame structure 520. The frame is divided into two main sections: a Roundtrip Delay Margin Window (520) and a Beamformer Analysis Window (530). The Roundtrip Delay Margin Window contains packets P₁ through P₉. The Beamformer Analysis Window contains packets P₆ through P₉. A Marker (505) is located at the end of the frame. The frame is also labeled 510.

Fig. 5

0907

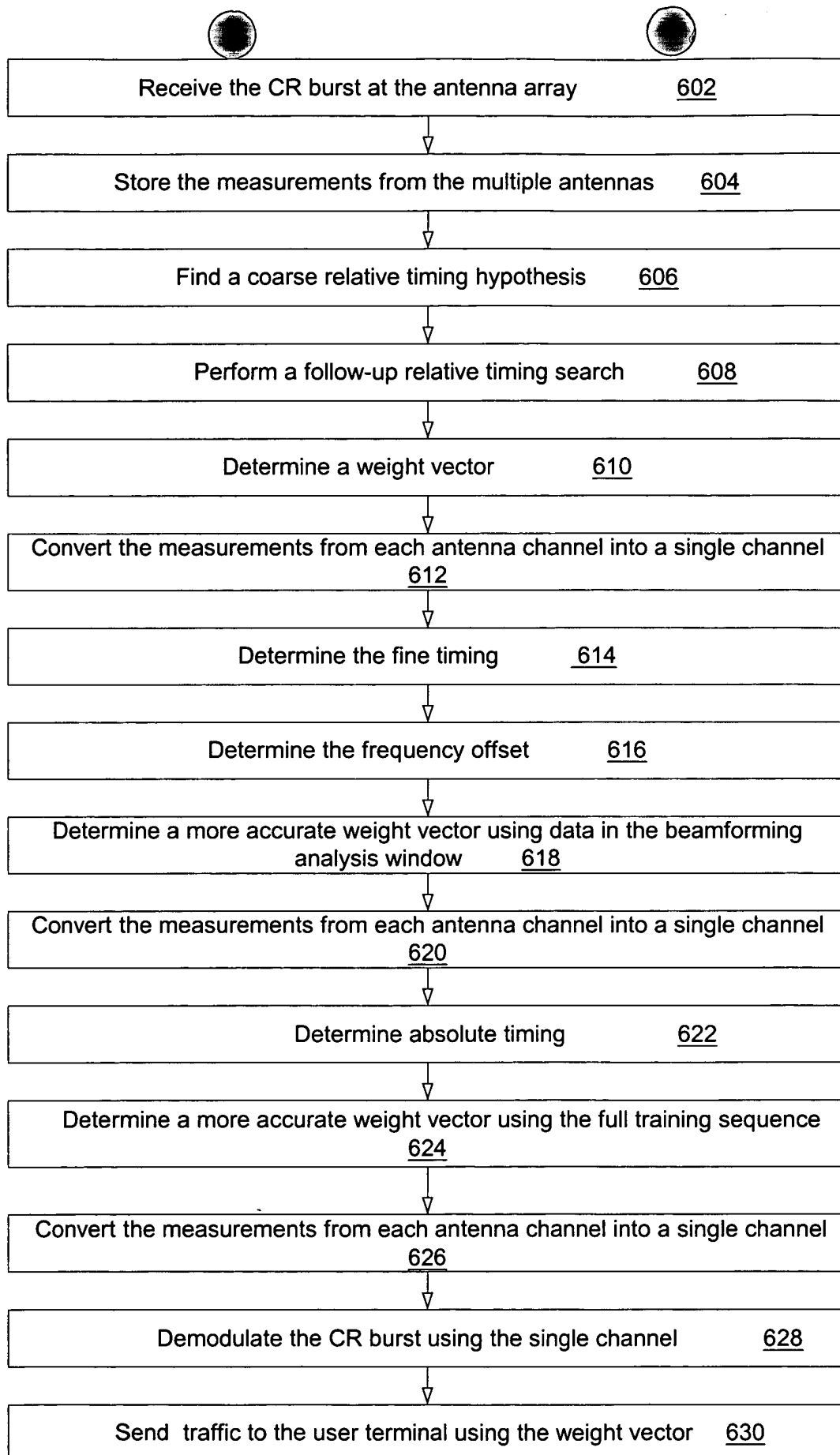


Figure 6

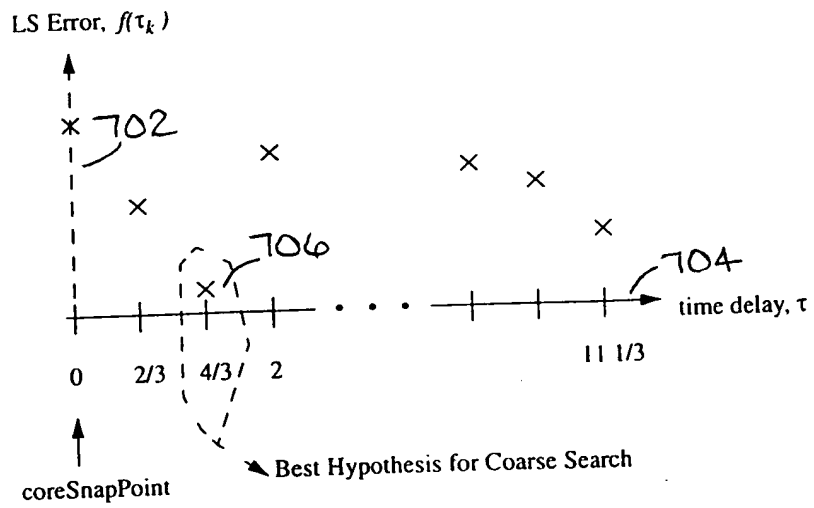


Fig. 7

